

IN THE U.S. PATENT & TRADEMARK OFFICE

By electronic submission

First Named Inventor: Patrick J. Meaney et al. : GROUP ART UNIT 2825

Pat Appln: Serial No. 10/817,279: Examiner: Phallaka Kik

5 Filed: April 2, 2004 : April 5, 2007

Title: DDRII Driver Impedance Adjustment Control Algorithm and Interface Circuits:

Attorney Docket POU920030010USI :

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AMENDMENT (Responsive to OA Dated 1/05/2007)

Commissioner for Patents

Washington DC 20231

15 Sir:

A. Introductory Comments:

In reply to the Official action mailed 1/05/2007

Applicants respectfully request entry of the following amendment

to place the application in condition for allowance after

20 reexamination of the present application. (The Commissioner is

also hereby authorized to charge payment of the fees associated

with this communication or credit any overpayment to Deposit

Account No. 09-0463. Those expected are already part of the

separate fee authorization.)